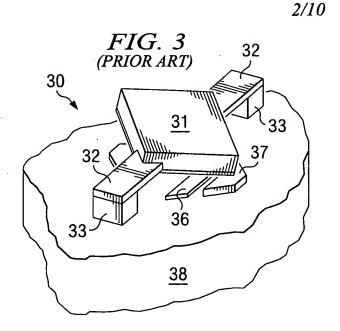
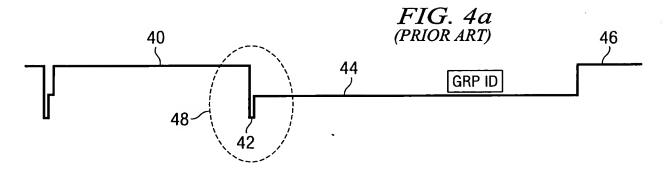
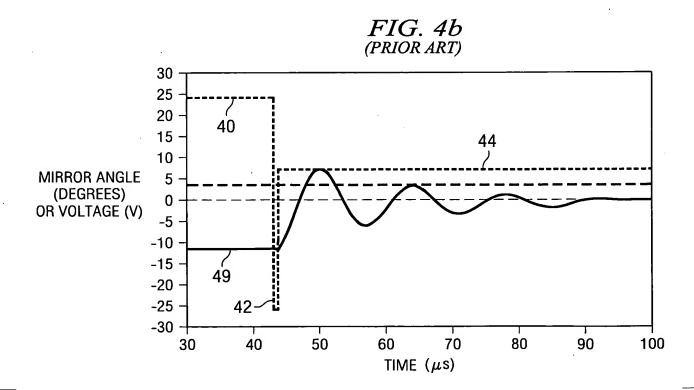


FIG. 2 (PRIOR ART) <u>21</u> 29 23 24 _~20 22 26⁾ 0 22 0 23 29 25 25 -28 28a TO CMOS

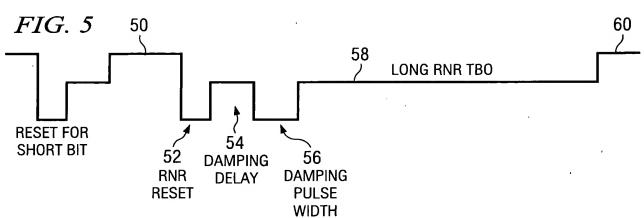


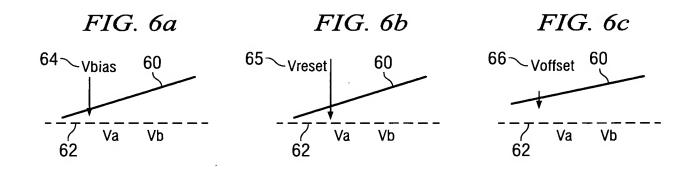












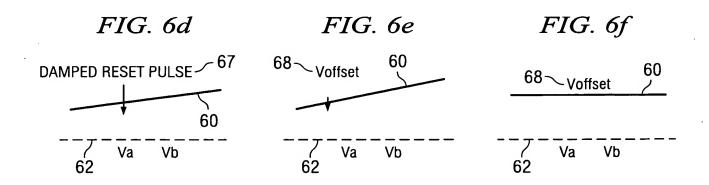
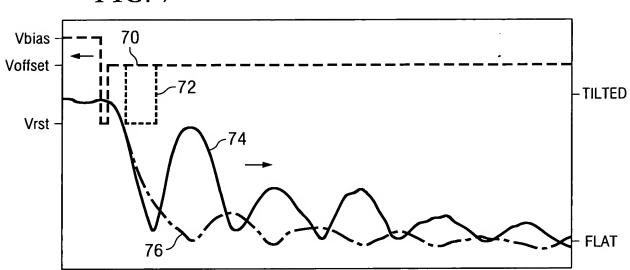
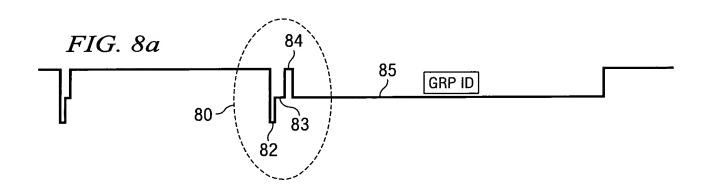
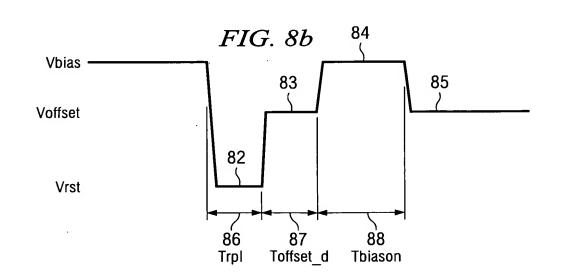




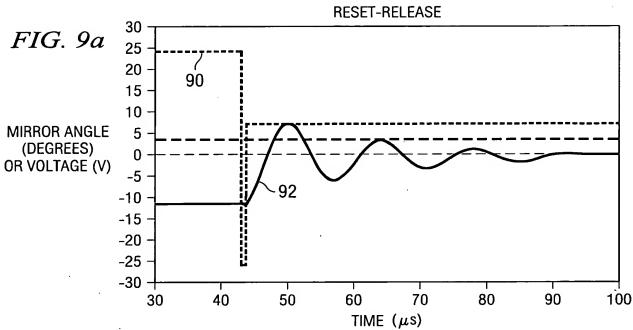
FIG. 7

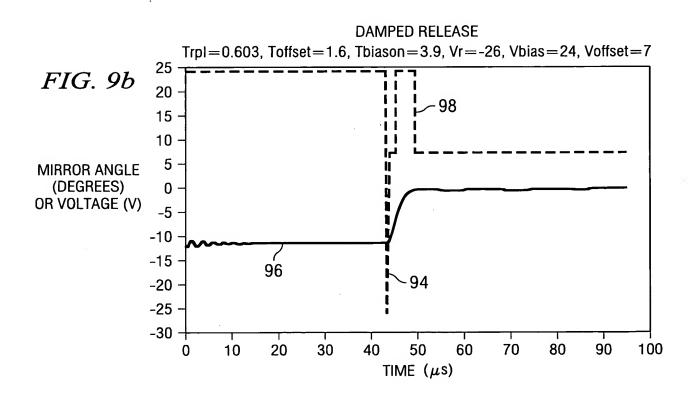
















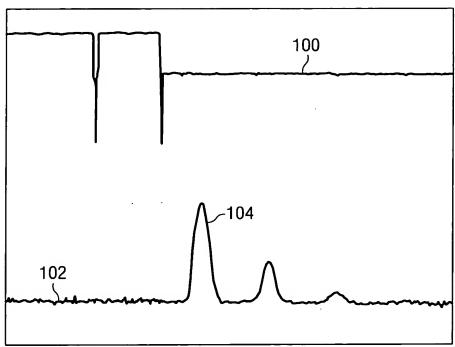


FIG. 10b

